

Substitute for form 1449A/PTO				<i>Complete if Known</i>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>				Application Number	
				Filing Date	
				First Named Inventor	Brian K. Langendorf
				Art Unit	2676
				Examiner Name	Tung, Kee M.
				Attorney Docket Number	42P6727C2
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U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
b77		US-5,892,964	04-06-1999	Horan et al.	
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		US-5,530,798	06-25-1996	Chu et al.	
		US-5,606,657	02-25-1997	Dennison et al.	
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v		US-6,211,891	04-03-2001	Wahlig	
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b77		US-6,167,476	12-26-2000	Olarig et al.	
		US-6,157,398	12-05-2000	Jeddeloh	
		US-			

Examiner Signature	K. Ting	Date Considered	9/22/04
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Sheet	2	of	2		

## **NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No.*	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
<i>PN</i>		Intel Corporation, "Accelerated Graphics Port Specification", Revised 2.0, May 4, 1998. Pages 1-29 and 31-77.	
<i>PN</i>		Intel Corporation, "A.G.P. Three Load Bus Issues and Analysis", Revision 0.4, January 20, 1998, pages 1-33.	

Examiner Signature	K. Tung	Date Considered	9/22/04
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